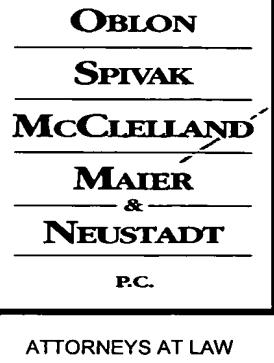




Docket No.: 216692US2



COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 09/995,594

Applicants: Takashi YAMADA, et al.

Filing Date: November 29, 2001

For: SEMICONDUCTOR CHIP WHICH COMBINES
BULK AND SOI REGIONS AND SEPARATES
SAME WITH PLURAL ISOLATION REGIONS

Group Art Unit: 2826

Examiner: DICKEY, THOMAS L

SIR:

Attached hereto for filing are the following papers:

COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR ALLOWANCE

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R. 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Katherine D. Pauley
Eckhard H. Kuesters
Registration No. 28,870
Attorney of Record

Customer Number

22850

(703) 413-3000 (phone)
(703) 413-2220 (fax)

Katherine D. Pauley
Registration No. 50,607

DOCKET NO: 216692US2



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

TAKASHI YAMADA, ETAL.

: EXAMINER: DICKEY, THOMAS L.

SERIAL NO: 09/995,594

:

FILED: NOVEMBER 29, 2001

: GROUP ART UNIT: 2826

FOR: SEMICONDUCTOR CHIP WHICH
COMBINES BULK AND SOI REGIONS
AND SEPARATES SAME WITH PLURAL
ISOLATION REGIONS

COMMENTS ON EXAMINER'S STATEMENT
OF REASONS FOR ALLOWANCE

COMMISSIONER FOR PATENTS

ALEXANDRIA, VIRGINIA 22313

SIR:

The "Statement of Reasons for Allowance" included at page 11 of the Office Action dated September 2, 2003 states "the first, second, and third isolations being of substantially the same depth, the first, second, and third isolations being deeper than the buried insulator, as recited in Claim 7" Applicants note that "the first, second, and third isolations being of substantially the same depth" is not recited in Claim 7, and Applicants therefore respectfully request that a revised Statement of Reasons for Allowance be issued without reference to the subject matter not recited in the claims.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Katherine D. Pauley

Eckhard H. Kuesters
Registration No. 28,870
Attorney of Record
Katherine D. Pauley
Registration No. 50,607

22850

Tel No: (703) 413-3000

Fax No: (703) 413-2220

EHK:KDP:pja

DOCKET NO.: 216692US2/hyc



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Takashi YAMADA, et al.

SERIAL NUMBER: 09/995,594 GROUP: 2826

FILED: November 29, 2001 EXAMINER: Thomas L. DICKEY

FOR: SEMICONDUCTOR CHIP WHICH COMBINES BULK AND SOI REGIONS AND
SEPARATES SAME WITH PLURAL ISOLATION REGIONS

REQUEST TO CORRECT TITLE OF INVENTION

MAIL STOP ISSUE FEE
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

SIR:

In the matter of the above-identified application for patent, we hereby request correction of your records to reflect the correct title of the invention. The title of the invention should read as follows: **SEMICONDUCTOR CHIP WHICH COMBINES BULK AND SOI REGIONS AND SEPARATES SAME WITH PLURAL ISOLATION REGIONS.**

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.



Marvin J. Spivak
Registration No. 24,913

Joseph A. Scafetta, Jr.
Registration No. 26, 803

Customer Number

22850

Tel. (703) 413-3000

Fax. (703) 413-2220

(OSMMN 05/04)

I:\USER\HYCHO\NOA\REQUESTCORRECT\216692.DOC